

FIGURE 1. (PRIOR ART)

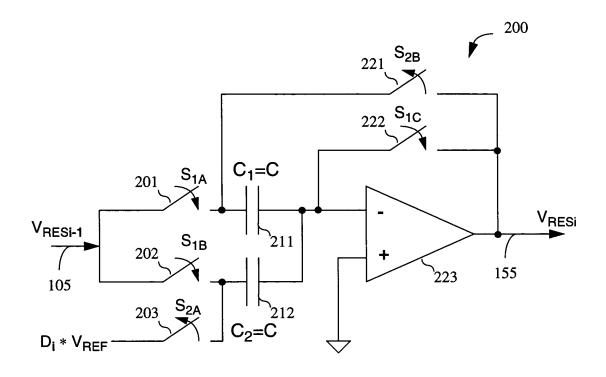


FIGURE 2. (PRIOR ART)

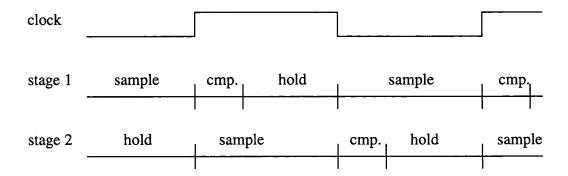


FIGURE 3. (PRIOR ART)

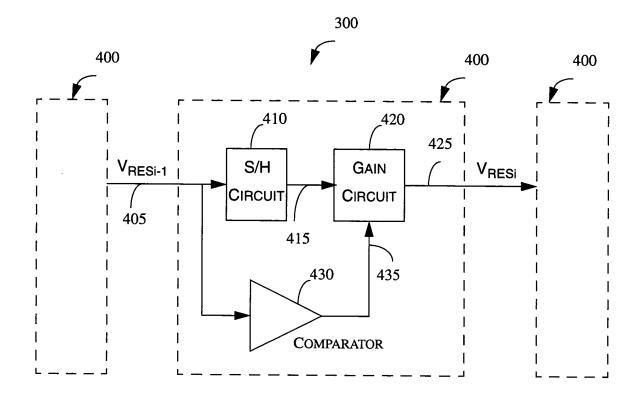


FIGURE 4.

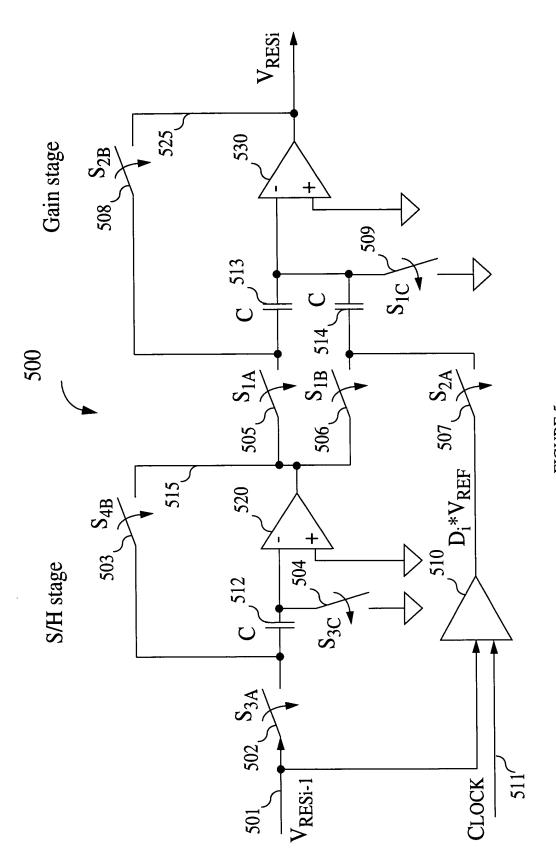


FIGURE 5.

clock				
s/h stage	sample	hold	sample	hold
comparator	sample	compare	sample	compare
gain stage	hold	sample	hold	sample

FIGURE 6.